

**AMENDMENTS TO THE ABSTRACT**

Please substitute the following paragraph(s) for the abstract now appearing in the currently filed specification:

In the present invention, during an 1H period excluding a blanking period (1HB) constituting a line display period, pixel data pulses of RGB (61B to 61R) are successively supplied for each color to corresponding signal lines for the color display of one pixel line. A control circuit (40) of select switches connected to the signal lines (6-1 to 6-n) supplies permission pulses (63B to 63R) for the supply of data to signal lines when displaying one color among RGB to select switches (TMG), and turns on the select switch (TMG) of the signal line corresponding to another color to be displayed later in the same line display period during the period of this application by a precharge pulse (62G or 62R) having a time duration shorter than the supply time of the pixel data of the other color (T2 or T3) to previously precharge the signal line of the other color to the predetermined potential. Due to this, sufficient precharging of a signal line<sub>1</sub> which became difficult due to an increase of a load capacitance of the signal line due to higher definition of the image display device and an increase in the speed of a drive clock thereof, can be achieved.